Pin allocation of KST Digital I/O Registers 25.8.2000

MPV924 ***register 1*** *at all sites are used only for ACU and POLARIZER control.*

MPV924 Register 1 VME slot 11 ACU and POLARIZER					
Channel	Signal	Description	User	MPV	Obs.
CH_00	S/C	Set complete	ACU	IN	Byte1, Addr C_1
CH_01	D/I	Data interrupt	ACU	IN	
CH_02	C/I	Command interrupt	ACU	IN	
CH_03	ARDY	Amplitude ready	POL	IN	
CH_04	PRDY	Phase ready	POL	IN	
CH_05					
CH_06					
CH_07					
CH_08	RA0	Read Address LSB	ACU	IN	byte_0 Addr C_0
CH_09	RA1	Read Address	ACU	IN	
CH_10	RA2	Read Address	ACU	IN	
CH_11	RA3	Read Address MSB	ACU	IN	
CH_12					
CH_13					
CH_14	RPAR	Read parity	ACU	IN	
CH_15	RD16	Read Data MSB	ACU	IN	
CH_16	RD0	Read Data LSB	ACU	IN	byte_1, Addr C_3
CH_17	RD1	Read Data	ACU	IN	
CH_18	RD2	Read Data	ACU	IN	
CH_19	RD3	Read Data	ACU	IN	
CH_20	RD4	Read Data	ACU	IN	
CH_21	RD5	Read Data	ACU	IN	
CH_22	RD6	Read Data	ACU	IN	
CH_23	RD7	Read Data	ACU	IN	
CH_24	RD8	Read Data	ACU	IN	byte_0, Addr C_2
CH_25	RD9	Read Data	ACU	IN	
CH_26	RD10	Read Data	ACU	IN	
CH_27	RD11	Read Data	ACU	IN	
CH_28	RD12	Read Data	ACU	IN	
CH_29	RD13	Read Data	ACU	IN	
CH_30	RD14	Read Data	ACU	IN	
CH_31	RD15	Read Data	ACU	IN	
CH_32	TD0	Transmit Data LSB	ACU	OUT/INV	Byte1, Addr C_5
CH_33	TD1	Transmit Data	ACU	OUT/INV	
CH_34	TD2	Transmit Data	ACU	OUT/INV	
CH_35	TD3	Transmit Data	ACU	OUT/INV	
CH_36	TD4	Transmit Data	ACU	OUT/INV	
CH_37	TD5	Transmit Data	ACU	OUT/INV	
CH_38	TD6	Transmit Data	ACU	OUT/INV	
CH_39	TD7	Transmit Data	ACU	OUT/INV	
CH_40	TD8	Transmit Data	ACU	OUT/INV	byte_0 Addr C_4
CH_41	TD9	Transmit Data	ACU	OUT/INV	
CH_42	TD10	Transmit Data	ACU	OUT/INV	
CH_43	TD11	Transmit Data	ACU	OUT/INV	
CH_44	TD12	Transmit Data	ACU	OUT/INV	
CH_45	TD13	Transmit Data	ACU	OUT/INV	
CH_46	TD14	Transmit Data	ACU	OUT/INV	
CH_47	TD15	Transmit Data	ACU	OUT/INV	
CH_48	R/W	Read/Write	ACU	OUT/INV	byte_1, Addr C_7

CH_49	STR	**STROBE**	ACU	OUT/INV	
CH_50					
CH_51					
CH_52					
CH_53					
CH_54					
CH_55					
CH_56	TA0	Transmit Address	ACU	OUT/INV	byte_0, Addr C_6
CH_57	TA1	Transmit Address	ACU	OUT/INV	
CH_58	TA2	Transmit Address	ACU	OUT/INV	
CH_59	TA3	Transmit Address	ACU	OUT/INV	
CH_60					
CH_61					
CH_62	TPAR	Transmit Parity	ACU	OUT/INV	
CH_63	TD16	Transmit Data MSB	ACU	OUT/INV	
CH_64	TDA0	Transmit amplitudi D	POL	OUT	Byte1, Addr C_9
CH_65	TDA1	Transmit amplitudi D	POL	OUT	
CH_66	TDA2	Transmit amplitudi D	POL	OUT	
CH_67	TDA3	Transmit amplitudi D	POL	OUT	
CH_68	TDA4	Transmit amplitudi D	POL	OUT	
CH_69	TDA5	Transmit amplitudi D	POL	OUT	
CH_70	TDA6	Transmit amplitudi D	POL	OUT	
CH_71	TDA7	Transmit amplitudi D	POL	OUT	
CH_72	ARUN	Amplitude RUN	POL	OUT	byte_0 Addr C_8
CH_73					
CH_74					
CH_75					
CH_76					
CH_77					
CH_78					
CH_79					
CH_80	TDP0	Transmit phase D	POL	OUT	byte_1, Addr C_B
CH_81	TDP0	Transmit phase D	POL	OUT	
CH_82	TDP0	Transmit phase D	POL	OUT	
CH_83	TDP0	Transmit phase D	POL	OUT	
CH_84	TDP0	Transmit phase D	POL	OUT	
CH_85	TDP0	Transmit phase D	POL	OUT	
CH_86	TDP0	Transmit phase D	POL	OUT	
CH_87	TDP0	Transmit phase D	POL	OUT	
CH_88	TDP0	Transmit phase D	POL	OUT	byte_0, Addr C_A
CH_89	PRUN	Phase RUN	POL	OUT	
CH_90					
CH_91					
CH_92					
CH_93					
CH_94					
CH_95					

MPV924 ***register 2*** ***used for receiver control among other things***

MPV924 Register 2 VME slot 12					
Channel	Signal	Description	User	MPV	Obs.
CH_00	PHLOSS_1	Phase lock/loss LO1/1	REC	IN	Byte1, Addr C_1
CH_01	PHLOSS_2	Phase lock/loss LO1/2	REC	IN	
CH_02	PHLOSS_3	Phase lock/loss LO2	REC	IN	
CH_03					
CH_04					
CH_05					
CH_06					
CH_07					
CH_08					byte_0 Addr C_0
CH_09					
CH_10					
CH_11					
CH_12					
CH_13					
CH_14					
CH_15					
CH_16	CH1_ATT_0	Attenuator control CH1	REC	OUT	byte_1, Addr C_3
CH_17	CH1_ATT_1	Attenuator control CH1	REC	OUT	
CH_18	CH1_ATT_2	Attenuator control CH1	REC	OUT	
CH_19	CH1_ATT_3	Attenuator control CH1	REC	OUT	
CH_20	CH1_ATT_4	Attenuator control CH1	REC	OUT	
CH_21	CH1_ATT_5	Attenuator control CH1	REC	OUT	
CH_22					
CH_23					
CH_24	CH2_ATT_0	Attenuator control CH2	REC	OUT	byte_0, Addr C_2
CH_25	CH2_ATT_1	Attenuator control CH2	REC	OUT	
CH_26	CH2_ATT_2	Attenuator control CH2	REC	OUT	
CH_27	CH2_ATT_3	Attenuator control CH2	REC	OUT	
CH_28	CH2_ATT_4	Attenuator control CH2	REC	OUT	
CH_29	CH2_ATT_5	Attenuator control CH2	REC	OUT	
CH_30					
CH_31					
CH_32	C/RC	Noise injection	REC	OUT	Byte1, Addr C_5
CH_33	ON/OFF	Noise injection	REC	OUT	
CH_34	H_CAL	Noise injection	REC	OUT	
CH_35	V_CAL	Noise injection	REC	OUT	
CH_36	HOR_DOP	Doppler injection	REC	OUT	
CH_37	VER_DOP	Doppler injection	REC	OUT	
CH_38	BP_FILT	BP-filter on/pypass	REC	OUT	
CH_39					
CH_40					byte_0 Addr C_4
CH_41					
CH_42					
CH_43					
CH_44					
CH_45					
CH_46					
CH_47					
CH_48					byte_1, Addr C_7
CH_49					

CH_50					
CH_51					
CH_52					
CH_53					
CH_54					
CH_55					
CH_56					byte_0, Addr C_6
CH_57					
CH_58					
CH_59					
CH_60					
CH_61					
CH_62					
CH_63					
CH_64					Byte1, Addr C_9
CH_65					
CH_66					
CH_67					
CH_68					
CH_69					
CH_70					
CH_71					
CH_72					byte_0 Addr C_8
CH_73					
CH_74					
CH_75					
CH_76					
CH_77					
CH_78					
CH_79					
CH_80					byte_1, Addr C_B
CH_81					
CH_82					
CH_83					
CH_84					
CH_85					
CH_86					
CH_87					
CH_88					byte_0, Addr C_A
CH_89					
CH_90					
CH_91					
CH_92					
CH_93					
CH_94					
CH_95					