

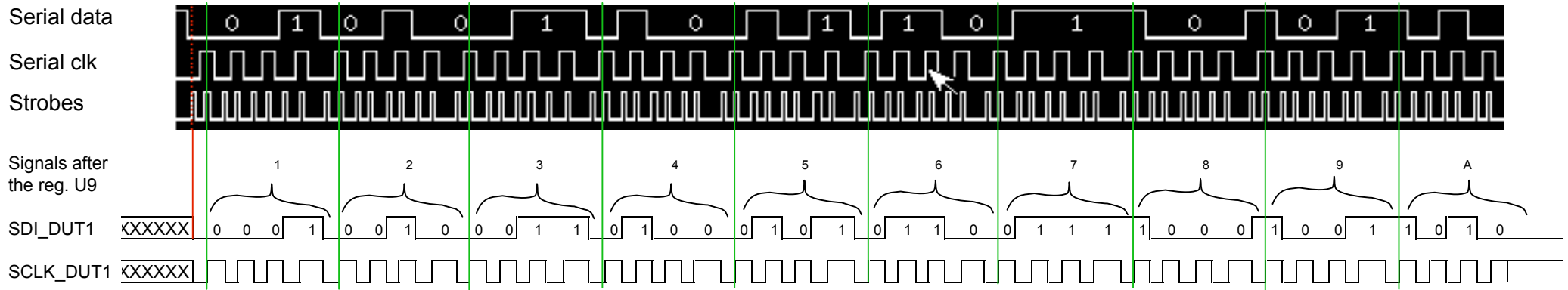
Testing dds_write program

30.9.2005/mjp

Command: `./dds_write -s 92 -c 49 -p -48 -l 16 -d 0x123456789A`

Output of the logic analyzer

Serial load using parallel I/O-interface



Rising edge of the SCLK clocks the data into the DDS-chip

