

ASF Counters memory map (ASF counter bit = "0" and AD3-AD0² according following list)

AD3	AD2	AD1	AD0	
0	0	0	0	
0	0	0	1	
0	0	1	0	Bit pulse counter I
0	0	1	1	
0	1	0	0	Sequency length counter I, lower byte (bits 7-0)
0	1	0	1	Sequency length counter I, upper byte (bits 11-8, bits 15-12 always "0")
0	1	1	0	
0	1	1	1	
1	0	0	0	
1	0	0	1	
1	0	1	0	Bit pulse counter II
1	0	1	1	
1	1	0	0	Sequency length counter II, lower byte (bits 7-0)
1	1	0	1	Sequency length counter II, upper byte (bits 11-8, bits 15-12 always "0")
1	1	1	0	
1	1	1	1	

*) AD3 - AD0 refers to the heating I/O pin allocation list's AD03 - AD00.