

DDS Carrier Boards 1-6

RC interface

Synchronization signals

Position: DDS VME crate 10-11, 12-13, 14-15, 16-17, 18-19, 20-21

J1			
PIN	ROW a	ROW b	ROW c
No:	Signal	Signal	Signal
1			
2			
3			
4			
5			
6			
7			
8			
9	GND		
10			
11	GND		
12			
13			
14			
15	GND		
16			
17	GND		
18			
19	GND		
20		GND	
21			
22			
23		GND	
24			
25			
26			
27			
28			
29			
30			
31	-12V	+5V/STB	+12V
32	+5V	+5V	+5V

J2			
PIN	ROW a	ROW b	ROW c
No:	Signal	Signal	Signal
1		+5V	DDIR
2		GND	LPRPint
3			GND
4			Sync_FUD
5			GND
6			Ena_sync_FUD
7			+Sync_CLK
8			-Sync_CLK
9			STMCx, (STSQ1)
10			GND
11			STMCx, (STSQ2)
12		GND	GND
13		+5V	
14			GND
15			
16			PS0, (PS02)
17			PS1, (PS12)
18			PatchClk
19			UPDx, (IOUP1)
20			UPDx, (IOUP2)
21			Running
22		GND	GND
23			DOF1 (not used)
24			GND
25			DOF2 (not used)
26			GND
27			
28	GA0		PS0, (PS01)
29	GA1		PS1, (PS11)
30	GA2		
31	GND	GND	GND
32		+5V	

NOTE! Red = RC-signal names
Black = signal names on the Carrier board

Remark: VME termination to all signals on the row J2c

